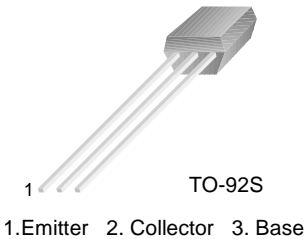




FJNS4211R

Switching Application (Bias Resistor Built In)

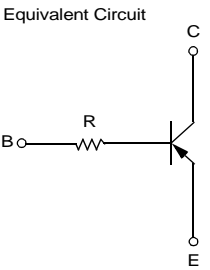
- Switching circuit, Inverter, Interface circuit, Driver Circuit
- Built in bias Resistor ($R=22K\Omega$)
- Complement to FJNS3211R



PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings $T_a=25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Value	Units
V_{CBO}	Collector-Base Voltage	-40	V
V_{CEO}	Collector-Emitter Voltage	-40	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current	-100	mA
P_C	Collector Power Dissipation	300	mW
T_J	Junction Temperature	150	$^{\circ}C$
T_{STG}	Storage Temperature	-55 ~ 150	$^{\circ}C$

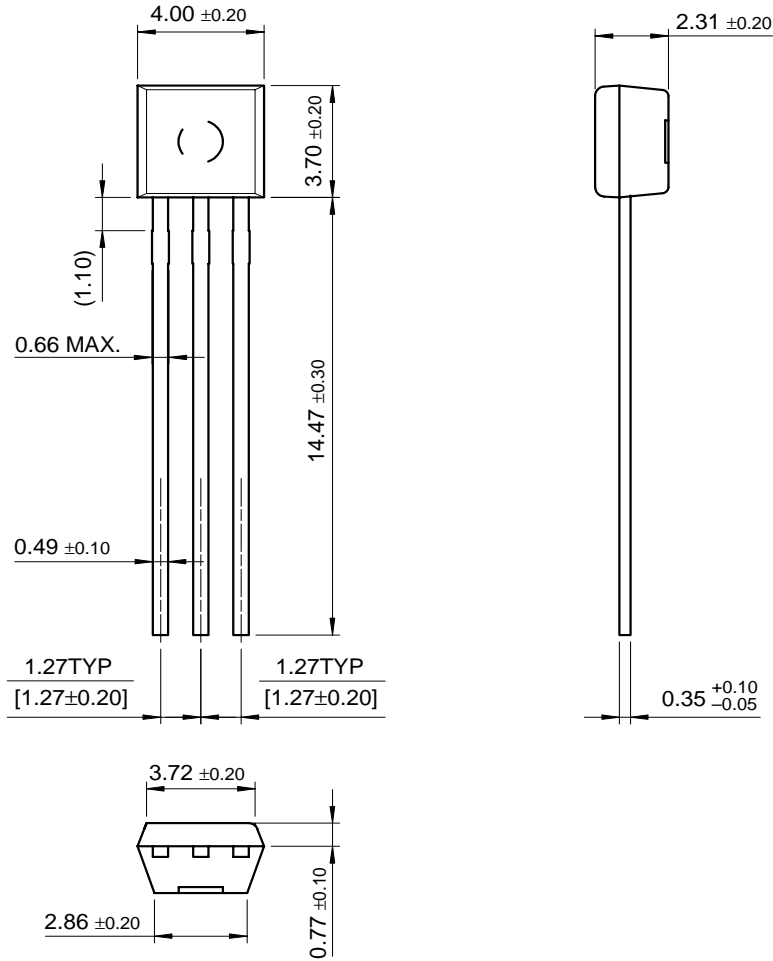


Electrical Characteristics $T_a=25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
BV_{CBO}	Collector-Base Breakdown Voltage	$I_C = -100\mu A, I_E = 0$	-40			V
BV_{CEO}	Collector-Emitter Breakdown Voltage	$I_E = -1mA, I_B = 0$	-40			V
I_{CBO}	Collector Cut-off Current	$V_{CB} = -30V, I_E = 0$			-0.1	μA
h_{FE}	DC Current Gain	$V_{CE} = -5V, I_C = -1mA$	100		600	
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = -10mA, I_B = -1mA$			-0.3	V
C_{ob}	Output Capacitance	$V_{CB} = -10V, I_E = 0$ $f = 1MHz$		5.5		pF
f_T	Current Gain Bandwidth Product	$V_{CE} = -10V, I_C = -5mA$		200		MHz
R	Input Resistor		15	22	29	K Ω

Package Dimensions

TO-92S



Dimensions in Millimeters

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